



OEM 8x32

digitizer	max. sampling frequency : 96 MHz (adjustable from 96 MHz to 6.6 MHz) range : 8bits input impedance : 50 Ω global delay : 0 up to 1.6 ms, step of 10 ns delay-laws at transmission/reception: 0 to 20 μ s, step of 2.5 ns digitizing depth : up to 8,000 samples
pulsers	adjustable voltage : 10 to 70V with 1V step negative rectangular pulse , adjustable width : 40 ns to 625 ns, step of 2.5 ns rise time < 10 ns (70V, 50 Ω) max. PRF : 10 KHz
receivers	bandwidth : 0.8 to 15 MHz, adjustable gain on each channel from 0 to 80 dB adjustable analog DAC on 80 dB (max. 40 dB/ μ s) synchronized on events cross-talk between two channels > 50 dB, max. input signal amplitude: 0.8 Vpp
embedded processors	FPGA on CPU-board
hardware configuration	Multiplexed architecture: 8x32
libraries	dll provided
dimensions	L x W x H : 201mm x 105mm x 53mm- Weight : ~600g
I-O	1 Hypertronix connectors, 3 encoders input, 1 external trigger 1 USB2, 2 LEMO connectors (type 00) External power supply input

